Control of Schottky and ohmic interfaces by unpinning Fermi level

S. Hara *, T. Teraji 1, H. Okushi, K. Kajimura 1

Electrotechnical Laboratory, 1-1-4 Umezono, Tsukuba, Ibaraki 305, Japan

Abstract

We propose the first systematical method to control Schottky barrier heights of metal/semiconductor interfaces by controlling the density of interface electronic states and the number of charges in the states. The density of interface states is controlled by changing the density of surface electronic states, which is controlled by surface hydrogenation and flattening the surface atomically. We apply establishing hydrogen termination techniques using a chemical solution, pH controlled buffered HF or hot water. Also, slow oxidation by oxygen gas was used to flatten resultant semiconductor surfaces. The density of interface charges is changeable by controlling a metal work function. When the density of surface states is reduced enough to unpin the Fermi level, the barrier height is determined simply by the difference between the work function of a metal and the flat-band semiconductor. In such an interface with the low density of interface states, an ohmic contact with a zero barrier height is formed when we select a metal with the work function lower than the flat-band Fermi level. We have already demonstrated controlling Schottky and ohmic properties by changing the pinning degree on silicon carbide surfaces. Moreover, we found the recovery of an ohmic property after TiC formation at Ti/6H-SiC interface at 700°C whereas conventional 5% HF rinsed Schottky Ti/6H-SiC interfaces still have Schottky properties after TiC formation.

Keywords: Fermi level pinning; Ohmic contact; Schottky contact; Monohydride; Interface states

1. Introduction

Ohmic contacts as non-rectifying electrodes and/or Schottky contacts as rectifying electrodes have been used in every discrete and integrated electronic devices since a diode was invented over half a century ago. Uncontrollability of Schottky barrier heights, however, restricts Schottky contacts within limited applications to only high speed devices such as Schottky diodes or metal–semiconductor field-effect transistors (MESFET); Rectifying role is usually made by p–n junctions. Also, the uncontrollability and inevitable formation of a Schottky barrier in every metal–semiconductor interface results in difficulty in the formation of an ohmic contact. In order to solve it, ohmic contacts are formed by leaking currents through the barriers instead of lowering the barrier heights. The leaky Schottky barriers have complex mechanisms on current transports because of complex interfacial atomic and electronic structures caused by interfacial reactions depending on experimental conditions. In practical devices, ohmic contacts by leaky Schottky bar-
carrier formation are fabricated by high doping of impurities so as to tunnel electron current through the barrier whose width is thinned. As a general trend, a Schottky barrier has been formed for an ideal abrupt interface, whereas an ohmic contact has been formed by deteriorating the interface abruptness or by lowering the crystallinity of the semiconductor. Conventional techniques to form Schottky and ohmic contacts are described in the text of Rhoderick [1].

Overcoming the uncontrollability will provide us wide applications of Schottky barriers as a stable function in devices and also ideal ohmic contacts with zero barrier height, which release us from difficulty in formation of the conventional ohmic contacts. The uncontrollability is mainly due to an interfacial Fermi-level pinning phenomenon which automatically determines the barrier height. Therefore, it is quite important to know the mechanism(s) of the pinning phenomenon and to develop how to unpin.

It is well-known that the Schottky barrier height \( \phi_{sn} \) is generally expressed as follows,

\[
\phi_{sn} = S ( \phi_m - \chi_s ) + C,
\]

where we define the slope parameter \( S \) as \( \partial \phi_{sn} / \partial \phi_m \). \( S \) has a range of \( 0 \leq S \leq 1 \). The situation with \( S = 1 \) is called the Schottky limit, where the Fermi level is free from pinning. When \( S = 0 \), \( \phi_{sn} \) in Eq. (1) becomes constant, and is independent of the metal work function \( \phi_m \). This situation of the strongest pinning is the Bardeen limit. Since in practical interfaces \( S \) has values between the Schottky limit and Bardeen limit, we can control the barrier height to some extent by changing a metal. For instance, the maximum and minimum barrier heights for silicide/n-Si interfaces are around 0.5 eV to 0.9 eV and thereby the maximum width of controlling the barrier height is around 0.4 eV [2], indicating that no ohmic interface is formed without high doping. So far, there existed no technique to control the pinning degrees, the Schottky barriers were out of control. As a result, each semiconductor has each fixed pinning degree.

In this report, we propose the first concept to unify the Schottky and ohmic formations by achieving the Schottky limit, where we utilize up-to-date chemical treatments to control surface termination. We also demonstrate barrier height controls and ohmic formations using 6H-SiC(0001) and the Si(111) substrates. Further, we demonstrate the thermal stability of the interfaces with Schottky limit.

2. Unifying Schottky and ohmic formations by controlling the Fermi level pinning

It is known that the pinning rate is dominated by the density of interface states \( D_s \). The relation is \( S = \epsilon_s / (\epsilon_s + q \delta D_s) \), where \( \epsilon_s \) is the permittivity of the semiconductor and \( \delta \) its thickness [2]. Therefore, we can control the degree of the Fermi level pinning by changing \( D_s \). In terms of lowering \( D_s \), in-situ cleaning of a semiconductor surface in ultrahigh vacuum is not effective because of reconstruction induced surface pinning [3]. This leads to pinning at resultant interfaces. So far, there is a significant report aiming control of \( S \) or \( D_s \) using ex-situ chemical treatment. Fan et al. reported that \( S \) of GaAs increases from 0.14 for conventional metal/GaAs interfaces to 0.53 by the sulfur termination of the surface [4]. The suggestive phenomenon in this technique is the retrieval of the controllability of \( S \) by controlling surface termination. If a semiconductor surface is a completely terminated one without no dangling bonds, it should endure oxidation and contamination, resulting in an ideal interface with low \( D_s \).

Recent techniques using chemical solutions to terminate semiconductor surfaces progress to reduce the density of surface states and make their surfaces flat atomically. Such an ideal surface should result in forming an interface with a low \( D_s \). We utilize two chemical solutions, pH modified buffered HF or hot water to suppress \( D_s \). pH modified buffered HF has been found to terminate the Si(111) surface only by monohydrides [5]. Hot water also has the same effect [6]. In a semiconductor surface layer with a low crystallinity which becomes the origin of high \( D_s \), removing the surface layer by an oxidation followed by a chemical etching is effective before the chemical termination. Also, slow oxidation is known to form an atomically-flat interface by enhancing a lateral oxidation speed relatively [7]. Atomicity-flat surface is obtained by removing the oxide layer. This surface has a lower density of surface states.

Further, an actual barrier height is determined by the number of interface charges \( Q_\alpha \) in \( D_s \). \( Q_\alpha \) is
changeable if the metal work function $\phi_m$ is changed because the change in Fermi levels between the metal and the semiconductor yields a charge transfer. When $D_n \to 0$, the interface is in the Schottky limit. In the limit with zero $D_n$, the ohmic property is obtained when $\phi_m$ is smaller than $\chi_e$ because $C$ in Eq. (1) is generally small. Since this ohmic interface has no Schottky barrier, the contact resistivity $\rho_c$ is zero, independent of the donor concentration $N_D$. In contrast, in a common ohmic contact, there exists a large $D_n$ resulting in a pinned Schottky barrier formation restricting the current conductivity. In such a contact, the minimum of $\rho_c$ is limited by the Schottky barrier width determined by $N_D$ because the current flows through the Schottky barrier by tunneling.

3. Demonstration of controlling barrier heights

Unintentionally doped n-type 6H-SiC (0001) bulk substrates grown by modified Lely method and nitrogen doped n-type 6H-SiC (0001) epitaxial layers were used. Si-faces of the SiC samples were used for all depositions and characterizations. The carrier concentrations $N_D$ determined by capacitance versus voltage ($C$–$V$) measurements were $\sim 2 \times 10^{17}$ cm$^{-3}$ for the bulk substrates and $\sim 5 \times 10^{17}$ cm$^{-3}$ for the epitaxial layers. Some of the samples were oxidized in a quartz tube furnace. The thickness of oxidized layers was about 3 nm. The oxidized layers were etched by dipping in 5% HF solution. Some of the samples with oxidation/etching treatment were dipped in boiling water or in pH-modified buffered HF for 10 min. Finally, the samples were rinsed in deionized water. Titanium, molybdenum, and nickel were deposited with an electron beam evaporator to form contact electrodes. During the depositions, the sample temperatures were kept below 100°C. No annealing was carried out for all electrodes. Magnesium ohmic back contacts were deposited at room temperature. Most significant experimental technique here is to avoid a procedure of back ohmic formation before forward electrode formation. Otherwise, in the following cleaning procedures the cleaning surfaces have effects of annealing history and/or contamination by the back metal or its cap wax. Actually, we have observed Ni Schottky contacts independent of the surface treatments when we form back ohmic contacts before Schottky electrodes [8].

Fig. 1 shows $\phi_{bn}$ as a function of $\phi_m$ for the three treatments. Conventional treatment (a) has a slope parameter $S$ of 0.2. After treatment (b) of oxidation followed by etching, $S$ rises to 0.8. Treatment (c) using boiling water generates 1.0, which is in the pinning-free Schottky limit. The Fermi level is being released from pinning in order with proceeding the treatments from (a) to (c). Ni contacts in all treatments show Schottky properties and $\phi_{bn}$ increases with proceeding the treatments from (a) to (c). In the Ti contacts $\phi_{bn}$ decreases with proceeding the treatments. $\phi_{bn}$ of the Ti contact by treatment (c) is a calculated value from $\rho_c$ of $6 \times 10^{-3}$ $\Omega$ cm$^{-2}$. In the Mo contacts, $\phi_{bn}$ for all treatments are almost the same. A cross point of the three lines near the Mo plots, indicating 0.70 eV of $\phi_{bn}$, is the pinning position of the Fermi level because it is independent of the surface treatments. This position is called a charge neutrality level. The corresponding $\phi_m$ at the charge neutrality level is around 4.6 eV in the figure. The extrapolated work function at zero Schottky barrier is 3.9 eV along the
line of \( S = 1 \). The work function is that of flat-band. Since \( S \) is constant for each treatment, \( \phi_{bn} \) is linear for the change in \( \phi_m \). In other words, \( S \) has no lowering behavior near the charge neutrality point at least in the metal/6H-SiC(0001) system.

When the Fermi level is at the charge neutrality point, the interface becomes electrically neutral. The amount of the band bending by the pinning 0.70 eV is preserved after the deposition of a metal with the work function of 4.6 eV. Also on the pinning-free surface, the metal with that of 4.6 eV also forms \( \phi_{bn} \) of 0.70 eV because a difference of work functions between SiC and the metal is 0.7 eV (\( = 4.6 - 3.9 \)). As the result, the metal at the charge neutrality point has the constant barrier height independent of the surface treatments by which the density of interface states widely varies.

We can estimate the electron affinity \( \chi_e \) using the observed flat-band work function 3.9 eV. Since the n-type dopant level by nitrogen is at 0.085 eV below the level of conduction band minimum \( [9] \), \( \chi_e \) is estimated at around 3.8 eV. This is a new way to estimate \( \chi_e \).

Schottky and ohmic contacts in our experiments are formed on the lightly doped substrates \( (N_d \sim 10^{17} \text{ cm}^{-3}) \). In such an interface with a wide depletion layer, a tunneling current is negligible. The dominant current transport is thermionic emission. Therefore, the ohmic currents in our experiments are generated by current flows over barriers with the low heights. Further, it should be noted that the ohmic contacts are formed without post-annealing.

We demonstrate the proposed technique also on Si(111) surfaces. According to the classical prediction of \( S \) by Kurtin et al. in 1969 \([10]\), \( S \) of Si is around strongly pinned 0.02, where we divide their value 0.05 by 2.27 to translate the metal parameter from the metal electronegativity to \( \phi_m \). In such an old period, the cleaning technique of semiconductor surfaces was not sophisticated well, resulting in the strong pinning. So, it is valuable to investigate \( S \) again using the present advanced techniques of surface cleaning. The hardness of Si, however, is much lower than SiC. The etching of the Si surface should be more careful. Otherwise, the resultant surface becomes rougher than that before the etching. So, we utilizes sophisticated procedures of cleaning to flatten the surface for Si(111).

Phosphorus-doped n-type Si(111) substrates with a carrier concentration of \( 7 \times 10^{16} \text{ cm}^{-3} \) were used. The mis-oriented angle was 0.44° toward \( \langle 110 \rangle \). All samples were degreased followed by 5% HF rinse for 60 s. A part of the samples were metallized by aluminum to make electrodes using the electron beam evaporator. Some of the other samples were metallized after immersion in pure water at 90°C. The rest samples just after the first 5% HF rinse were oxidized at 1100°C in the quartz tube furnace. The oxide thickness was 200 nm. The oxide layers were removed by 5% HF rinse. The samples were cleaned using the procedure of RCA clean \([11]\). After the cleaning, they were oxidized again at 300°C followed by 800°C to flatten the Si surfaces. The oxides were removed by 5% HF rinse. After that, RCA clean was performed again. The samples were also immersed in the hot water. Al was deposited after the procedure. Fig. 2 shows the \( I-V \) characteristics of the samples with the three procedures, the 5% HF
rinse, the hot water immersion, and the hot water immersion after the flat oxidation. The barrier height is decreasing with proceeding the procedure from the simple 5% HF through the hot water immersion to terminate the surface by monohydride to the procedure including the flat oxidation. Note that conventional oxidation at around 1000°C had no effect to decrease the barrier height. Also, a rectifying property, that is, the current difference between forward and reverse bias is disappearing. The samples with the flat oxidation followed by hot water immersion have the ohmic-like property because the current difference is within one order of magnitude. The remaining Schottky barrier of 0.51 eV for the last procedure is an obstacle to lower the contact resistivity.

If we know the electron affinity $\chi$ of Si, we can estimate an ideal barrier height for Al in the pinning-free condition. $\chi$, however, is a controversial parameter because it has no direct way to measure. If we use the value of 5.15 eV as the ionization energy [12], the estimated $\chi$ by subtracting the band gap width 1.12 eV is 4.03 eV. By adding 0.16 eV between Fermi level and conduction band minimum (for the carrier concentration of $7 \times 10^{16}$ cm$^{-3}$) to 4.03 eV, $\phi_{e-Si}$ of the flat-band is calculated to be 4.19 eV. The estimated ideal Schottky barrier in the pinning-free condition is 0.09 eV using the value of 4.28 eV for $\phi_{Al}$ [13]. This is much smaller than our observed 0.51 eV. This suggests that the surface with the flat oxidation followed by the hot water immersion still has an enough density of interface states to pin. A further advanced technique is required to lower the barrier height. Even in this unsatisfactory situation, it is surprising that the ohmic property is obtained by flattening the surface atomically and increasing the density of stable monohydride, which is an way completely contradictory to the conventional techniques to form ohmic contacts.

4. Thermal stability of pinning-controlled interfaces

Significant concern about the pinning-controlled interfaces is thermal stability of the interfaces with the ideal low densities of interface states. Is initial electronic ideality at an interface preserved after interface chemical reaction by heat treatment?

We prepared pinning-controlled Ti ohmic contacts onto 6H-SiC(0001) substrates formed by boiling water immersion and conventional Ti Schottky contacts onto the substrates formed by 5% HF rinse. These two sets of samples are annealed in argon atmosphere. Ti electrodes were capped by Ni to prevent from surface oxidation. The ohmic contacts have no change in $\rho_c$ until 400°C as shown in Fig. 3. Small widths of error bars suggest that $\rho_c$ is an essential value for the Ti/SiC contacts. $\rho_c$ increases drastically from 450°C and takes the maximum at 600°C. It surprisingly turns into decrease after 600°C. It was reported from transmission electron microscopy that epitaxial TiC forms at 700°C at the interface [14]. Since the ideal ohmic resistivities collapses in the regime between 450 to 600°C, atoms at the interface start to break interface chemical bonds as an precedent phenomenon of interdiffusion, resulting in increase in $D_{Si}$. In the next regime from 650°C, $D_{Si}$ decreases because of the recovery of interface ideality. On the other hand, the conventional Schottky contacts deteriorate from 550°C. It is known that a rectifying property at the conventional Ti/SiC interface recovers at 700°C for a longer anneal [14].
5. Conclusions

We proposed the first method to control Schottky barriers by changing the Fermi level pinning and by changing the metal work function. This method unifies Schottky barriers and ohmic conductivities in terms of their artificial control. It has a simple current transport mechanism with a minor effect of tunneling current. The pinning control was achieved by dipping silicon carbide crystals into pH modified buffered HF or boiling water before metallizations. These treatments have an effect to reduce the density of the surface states and the resultant density of the interface states. Ohmic interfaces were formed by selecting Ti with a low work function as a contact metal. Further, we found that Schottky barriers of Al contacts onto the Si(111) substrates with atomically-flat surfaces prepared using cleaning by hot water and a slow oxidation technique by oxygen gas are lowered toward the ohmic regime. Moreover, we observe a precedent interdiffusion phenomenon breaking interface chemical bonds before forming an interfacial TiC layer at the Ti/6H-SiC(0001) interface.

References